



A world class Electronic Design Automation (EDA) company, Helic, Inc. provides cutting-edge software tools and technology for designing chips, that are used in wireless communications, broadband networking, PCs, tablets, and other applications.

The constant shrinking of electronic devices and the chips that run them creates unwanted, problematic phenomena, which design teams cannot predict until they actually fabricate a chip; making for an exceedingly expensive test. Helic's software tools produce very accurate models that capture, predict, and help provide solutions to these phenomena.

Specifically, Helic develops disruptive EDA technologies for RFIC and high-speed System-on-Chip (SoC) design. We provide our customers with a comprehensive offering combining design tools, silicon intellectual property (IP) and applications support, greatly reducing the development cycles of chips for wireless communications, broadband networking, PCs, tablets and other segments. We provide technology for rapid electromagnetics modeling, RF component synthesis, and signal integrity of silicon ICs and Systems-in-Package.

Based in San Jose, USA, Helic was founded in 2000 as a Greek start-up by a group of young engineers. The company has 35 highly skilled professionals in its ranks today (indicatively, 1 in 3 hold a PhD); offices in the US, Greece, and Japan; a Research & Design center in Europe; and a sales presence, through partners, in Korea, Taiwan, Israel, and China.

Our first EDA product, VeloceRF™, was introduced in December 2003. Today VeloceRF™ is the industry's leading software product for inductor and transformer synthesis and modeling – both in terms of companies using it as well as in terms of installed licenses. Our product portfolio recently expanded with the introduction of VeloceRaptor/X™, a breakthrough EM analysis and extraction product that offers unprecedented modeling capacity and speed. VeloceRaptor/X™ makes the extraction of RLCK parasitics possible and produces netlists that can be simulated for complex IC designs targeting High Speed design applications. Our product suite is complemented with VeloceWired®, the industry's first EDA product to target package bondwire design and modeling, fully integrated in Custom IC design flows, while working in concert with IC design and modeling tools.

To date Helic has raised over \$7 million in funds from Venture Capitalists and from strategic and angel investors within the EDA industry such as:

- Magma Design Automation Inc., now part of Synopsys, one of the largest EDA companies;
- Fujitsu Ltd. Group, through its venture capital company Global Innovation Fund Partnership;
- Andy Bechtolsheim, renowned entrepreneur (co-founder of Sun Microsystems), technology visionary and engineer (Arista Networks), and angel investor (Google).

Helic's products have been adopted by more than 1,500 design engineers and over 60 companies worldwide, including many major semiconductor companies such as industry leaders Sony, Toshiba, Infineon Semiconductors, Freescale, Fujitsu, SHARP, and NetLogic Microsystems.

For additional information please visit [www.helic.com](http://www.helic.com).